

- Programmable post trigger sample count TMS320C6416 digital signal processor running at 1GHz.
- 128 megabytes of SDRAM organized as 32M x 64 on EMIFA.
- 100 MHz EMIFA operation, 50 MHz EMIFB operation.
- EMIFB and peripheral ports available on high-speed connectors for integration of custom I/O.
- DSP processing supported by booting DSP code from the cPCI host.
- cPCI host access to DSP features through provided drivers.
- Software source code provided.
- Support for developers with JTAG connector and PCI configuration EEPROM available.



Overview

The MH-C64DSP provides the digital signal processing power of the high-end Texas Instruments TMS320C6416 DSP chip with a complement of 128Mbyte of SDRAM. A fully outfitted expansion connector provides a ready platform for rapid integration of DSP applications. The expansion connector includes a second external memory interface (EMIFB), two serial ports, two timers, a complement of digital I/O, and power supplies. LabVIEW drivers offer the ability to load code, control processor operation, and transfer data to or from any area of DSP memory over the CompactPCI backplane. A complete schematic of the MH-C64DSP is included along with driver and application source code. LabVIEW drivers allow the use of features on the chip from the CompactPCI host. MH-C64DSP serves the dual purpose of a simple CompactPCI development system and a powerful expansion to DSP coprocessor and configurable I/O module.

Specifications

Dataway Interface

CompactPCI at 32 Bits and 33 MHz
Compliant with PICMG 2.0 R3.0
Single Width 3U CompactPCI Card

Processor

TMS320C6416 at 1GHz

Interface

EMIFB, Serial Ports, Timers, GPIO
Voltages including +3.3V, ±5V, ±12V

Ambient Temperature Range

0 to 70 C

Memory

128 Megabytes Organized 32M x 64